



ARCHITECTURE • ENGINEERING • INTERIOR DESIGN

## ADDENDUM

**PROJECT:** University of South Dakota  
Patterson Hall  
Detector Development Lab  
Vermillion, South Dakota

### ADDENDUM NUMBER

AD-1

### ISSUED BY:

Matt Metcalf, Architect  
Jon Dalton, Electrical Engineer

**PROJECT #:** 12-0049

**DATE ISSUED:** Tuesday, April 10, 2012

*This addendum is issued by the Architect to all known bidders before receipt of proposals, for the purpose of explaining, interpreting, or modifying the original plans and specifications. When enumerated by the bidder upon the proposal sheet, the information or instructions given hereon will be equally binding upon all parties as if included in the original plans and specifications.*  
**BIDDER MUST ENTER THE NUMBER OF THIS ADDENDUM ON HIS PROPOSAL SHEET**

### THE FOLLOWING ITEMS ARE APPLICABLE TO THE DRAWINGS:

#### AD-1, ITEM 1:

In reference to drawing Sheet A-103, note the following changes:

Provide a new furred out wall consisting of 2 ½” metal studs with one layer of 5/8” gypsum wall board to cover the exposed glazed tile block along the north wall. This will entail adding approximately 12’-0” new wall, starting just to the west of the glazed tile and extending to the new wall on the east side of this lab, floor to ceiling. Provide level 4 finish. Paint.

At two locations on exterior wall and one location on east wall where demolition of existing CMU wall has left gaps in the wall surface, provide infill using 2 ¼” solid concrete masonry units and grout as required to create a finished surface. Paint.

At floor location where porcelain tile demolition has left a rough, irregular surface, provide floor leveling compound as required for new floor surface.

At corridor location, provide 4” lightweight CMU infill above existing CMU wall to bring this wall to structure. Provide UL approved, one-hour rated fire caulking as needed at all penetrations.

#### AD-1, ITEM 2:

In reference to drawing Sheet E-102, revise Keyed Note 30 by add sentence reading “Inverter shall be Eaton Ferrups UPS #FE4.3kVA. Unit shall have an input and output voltage of 120.”

**END AD-1**